

| Ref # | Hits   | Search Query                           | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|--------|--|---|------------------|---------|------------------|
| L4    | 610    | 335/78.ccls.                           | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/09/15 15:35 |
| L5    | 128769 | (test\$4 or determin\$4) near10 switch | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/09/15 16:27 |
| L6    | 33     | 4 and 5                                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/09/15 15:36 |
| L7    | 70321  | switch with coil                       | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/09/15 15:36 |
| L8    | 16     | 6 and L7                               | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/09/15 15:38 |
| L9    | 307    | 335/78-86, 177-183.ccls.               | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/09/15 15:39 |
| L10   | 16     | 5 and 9                                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/09/15 15:41 |
| L11   | 3113   | switch near10 wafer                    | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/09/15 15:41 |
| L12   | 571    | 5 and 11                               | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/09/15 16:16 |
| L13   | 0      | 9 and 12                               | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/09/15 15:42 |

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| L14 | 9765 | 324/760,763,765,158.1.ccls.        | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 15:42 |
| L15 | 0    | 9 and L14                          | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 15:43 |
| L16 | 5160 | 324/158.1.ccls.                    | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 15:43 |
| L17 | 0    | 9 and L16                          | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 15:43 |
| L18 | 33   | 12 and 14                          | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 16:05 |
| L19 | 87   | 12 and coil                        | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 16:05 |
| L20 | 90   | 12 and relay                       | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 16:20 |
| L21 | 356  | relay near5 wafer                  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 16:24 |
| L22 | 9    | (test\$4 or determin\$4) near10 21 | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 16:21 |
| L23 | 0    | relay near5 "on the wafer"         | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 16:24 |

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| L24 | 0      | switch near5 "on the wafer"                          | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 16:26 |
| L25 | 0      | switch near10 "on the wafer"                         | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 16:25 |
| L26 | 0      | chip near10 "on the wafer"                           | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 16:25 |
| L27 | 2139   | magnet with wafer                                    | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 16:26 |
| L29 | 141180 | (test\$4 or determin\$4) near10<br>(switch or relay) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 16:27 |
| L30 | 80901  | 29 and "30"  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 16:27 |
| L31 | 19     | 29 and 27  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 16:28 |
| S1  | 2154   | switch near5 wafer                                   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 10:54 |
| S2  | 29     | switch near5 cantilever near5 coil                   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 11:02 |
| S3  | 2      | S1 and S2  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 10:55 |

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| S4  | 610    | 335/78.ccls.  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 11:02 |
| S5  | 9      | S1 and S4   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 11:03 |
| S6  | 128554 | (determin\$4 or test\$4) near10<br>switch                           | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 11:05 |
| S7  | 70268  | switch with coil  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 11:05 |
| S8  | 7929   | S6 and S7   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 11:05 |
| S9  | 53     | S1 and S8   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 11:12 |
| S10 | 2      | (test\$4 or determin\$4) near5 (micro<br>adj2 magnetic adj2 switch) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 11:13 |
| S11 | 241    | (test\$4 or determin\$4) near5<br>(magnetic adj2 switch)            | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 15:35 |
| S12 | 5155   | 324/158.1.ccls.   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 11:13 |
| S13 | 0      | S11 and S12   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 11:13 |

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| S14 | 1      | S11 and S4                                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 11:13 |
| S15 | 2      | S11 and S1                                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 13:43 |
| S16 | 9752   | 324/760,763,765,158.1.ccls.               | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 13:43 |
| S17 | 128554 | (determin\$4 or test\$4) near10<br>switch | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 13:44 |
| S18 | 928    | S17 and S16                               | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 13:49 |
| S19 | 2154   | switch near5 wafer                        | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 13:49 |
| S20 | 18     | S19 and S18                               | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 13:53 |
| S21 | 70268  | switch with coil                          | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 13:53 |
| S22 | 31     | S18 and S21                               | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 13:59 |
| S23 | 610    | 335/78.ccls.                              | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 14:00 |

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| S24 | 0    | S18 and S23                                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 14:00 |
| S25 | 871  | 438/48.ccls.                               | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 14:00 |
| S26 | 0    | S18 and S25                                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 14:02 |
| S27 | 1655 | determin\$4 with cantilever                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 14:02 |
| S28 | 33   | determin\$4 near10 cantilever with state   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 14:03 |
| S29 | 0    | S28 and S16                                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 14:04 |
| S30 | 2    | S28 and S17                                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 14:05 |
| S31 | 26   | coil with switch adj cantilever with state | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 14:06 |
| S32 | 0    | S16 and S31                                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/12 14:06 |
| S33 | 6    | S17 and S31                                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/09/15 15:34 |